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Snider

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(54) **MOLECULAR-JUNCTION-NANOWIRE-CROSSBAR-BASED NEURAL NETWORK**

(75) Inventor: **Greg Snider**, Mountain View, CA (US)

(73) Assignee: **Hewlett-Packard Development Company, L.P.**, Houston, TX (US)

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This patent is subject to a terminal disclaimer.

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(52) **U.S. Cl.** **706/26; 706/15; 706/27; 977/938**

(58) **Field of Classification Search** **706/26, 706/29, 15, 27; 716/17; 977/712, 839, 938; 327/365**

See application file for complete search history.

(56) **References Cited**

U.S. PATENT DOCUMENTS

2,835,829 A * 5/1958 Chollet et al. 327/487

4,823,174 A * 4/1989 Itoh et al. 257/198

6,128,214 A * 10/2000 Kuekes et al. 365/151
6,314,019 B1 11/2001 Kuekes et al.
6,735,579 B1 * 5/2004 Woodall 706/20
6,889,216 B2 5/2005 Nugent
2003/0200521 A1 * 10/2003 DeHon et al. 716/16
2003/0236760 A1 * 12/2003 Nugent 706/26
2004/0039717 A1 2/2004 Nugent
2004/0041617 A1 * 3/2004 Snider et al. 327/365

OTHER PUBLICATIONS

Gudiksen, Mark S., et al., "Growth of nanowire superlattice structures for nanoscale photonics and electronics", Nature, vol. 415, Feb. 7, 2002.

* cited by examiner

Primary Examiner—Joseph P Hirl

(57) **ABSTRACT**

A method for configuring nanoscale neural network circuits using molecular-junction-nanowire crossbars, and nanoscale neural networks produced by this method. Summing of weighted inputs within a neural-network node is implemented using variable-resistance resistors selectively configured at molecular-junction-nanowire-crossbar junctions. Thresholding functions for neural network nodes are implemented using pFET and nFET components selectively configured at molecular-junction-nanowire-crossbar junctions to provide an inverter. The output of one level of neural network nodes is directed, through selectively configured connections, to the resistor elements of a second level of neural network nodes via circuits created in the molecular-junction-nanowire crossbar. An arbitrary number of inputs, outputs, neural network node levels, nodes, weighting functions, and thresholding functions for any desired neural network are readily obtained by the methods of the present invention.

17 Claims, 17 Drawing Sheets

